IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:	Confirmation No. 6902
Ketan Padalia et al.	Examiner: Nghia M. Doan
Application No.: 10/716,309	Technology Center/Art Unit: 2825
Filed: November 17, 2003	SUPPLEMENTAL AMENDMENT
For: TECHNIQUES FOR GROUPING CIRCUIT ELEMENTS INTO LOGIC BLOCKS	
Customer No.: 26059	

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to examination of the above-referenced application, please enter the following amendments and remarks:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 8 of this paper.